

## EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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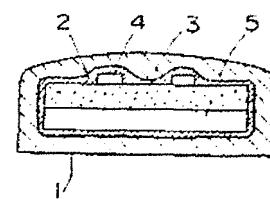
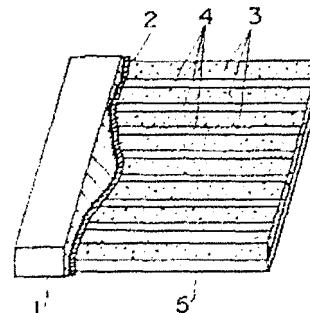
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APPLICANT : NEC CORP;

INVENTOR : KAWASAKI SETSUKO;

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**TITLE : COMPOSITE CIRCUIT SUBSTRATE**



**ABSTRACT :** PURPOSE: To make it possible to reduce the whole area of a substrate, by providing a signal line on one surface of a substrate, surrounding an input part and an output part of the signal line with a magnetic substance, providing an earth part on the other surface of the substrate and interposing a dielectric substance between the earth part and the signal line.

CONSTITUTION: An input part and an output part of a signal line 4 are collectively surrounded with a magnetic substance 1, respectively, an earth part 5 is provided on a surface on which the signal line of the substrate is not provided or in an inside thereof, and a dielectric substance 3 is interposed between the earth part 5 and the signal line 4, on a circuit substrate having an interface of a signal, thereby to provide a function of a common mode choke and a function of a capacitor between the earth part 5 and the signal line 4. As a result, it is possible to reduce an area of a substrate.

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